Abstracts

A design approach for mass producible high-bit-rate MMIC transimpedance amplifiers

E.M. Bastida, V. Corso, C.A. Finardi, R.A. Fischer and V. Patiri. "A design approach for mass producible high-bit-rate MMIC transimpedance amplifiers." 1997 Microwave and Guided Wave Letters 7.10 (Oct. 1997 [MGWL]): 317-319.

A full design approach for developing very highspeed transimpedance amplifier (TIA) monolithic microwave integrated circuits (MMICs) to be economically produced in large quantities is described. As an application example, the letter reports design, experimental performances, yield, and size data for a 5-Gb/s MMIC TIA using a low-cost 0.5-/spl mu/m GaAs field-effect transistor (FET) technology, showing outstanding experimental performances and optimized for a large-volume industrial production.

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